

(PRIOR ART)

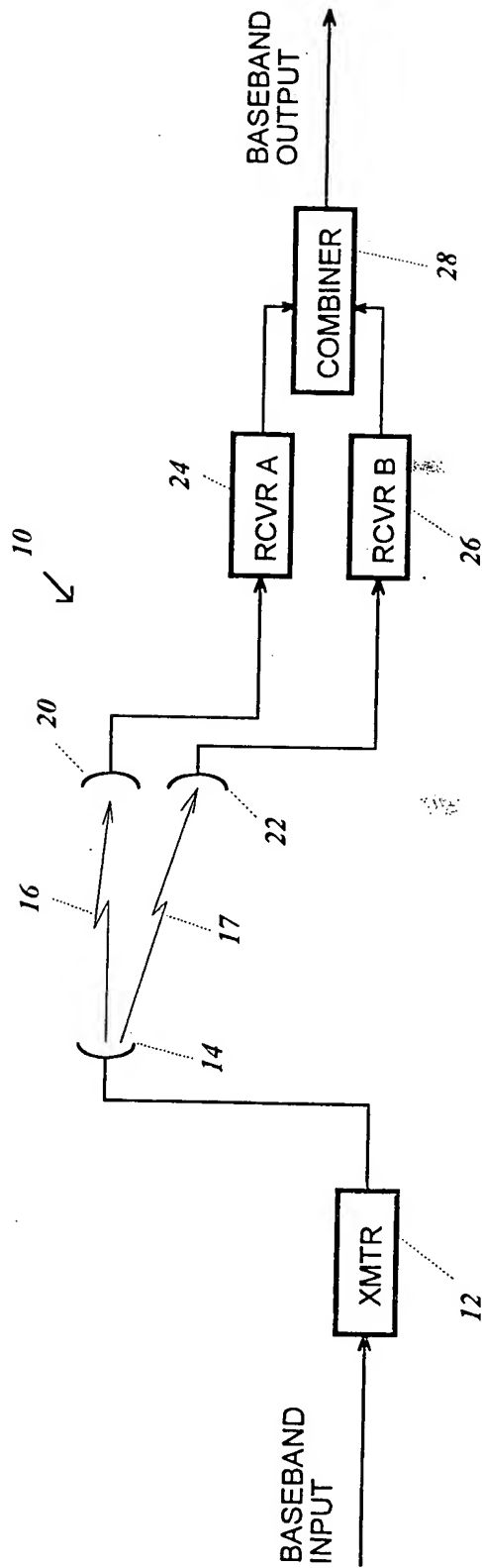


Fig. 2

(PRIOR ART)

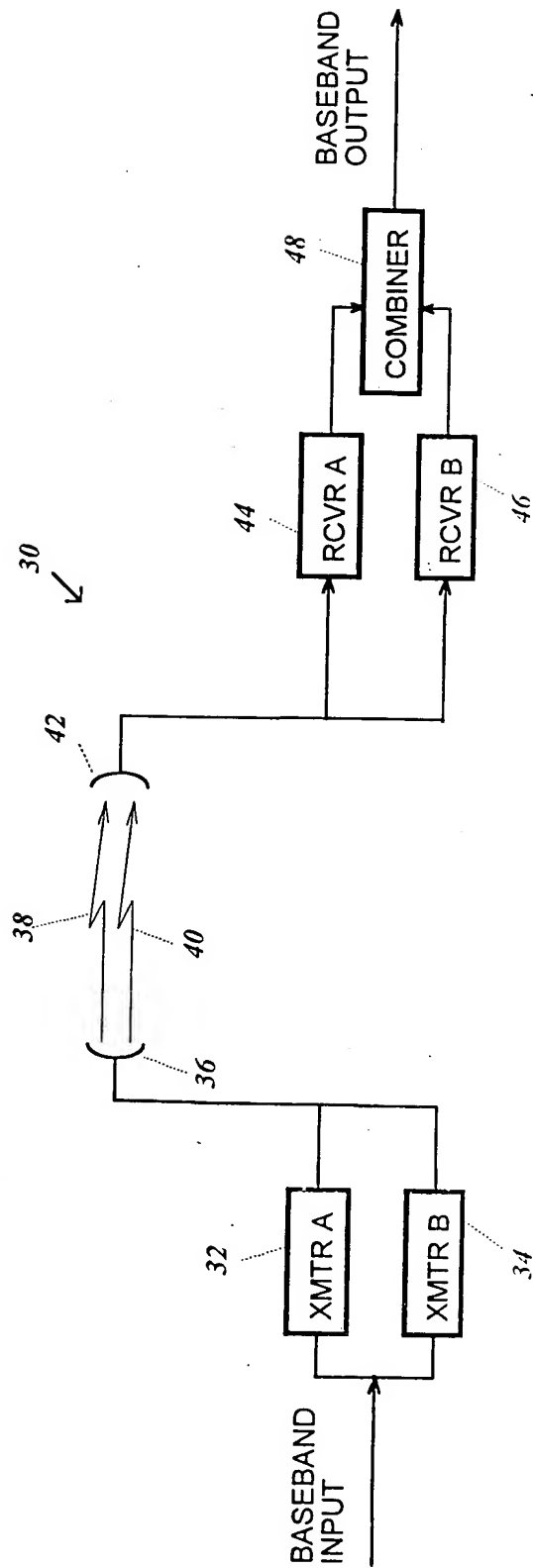


Fig. 3

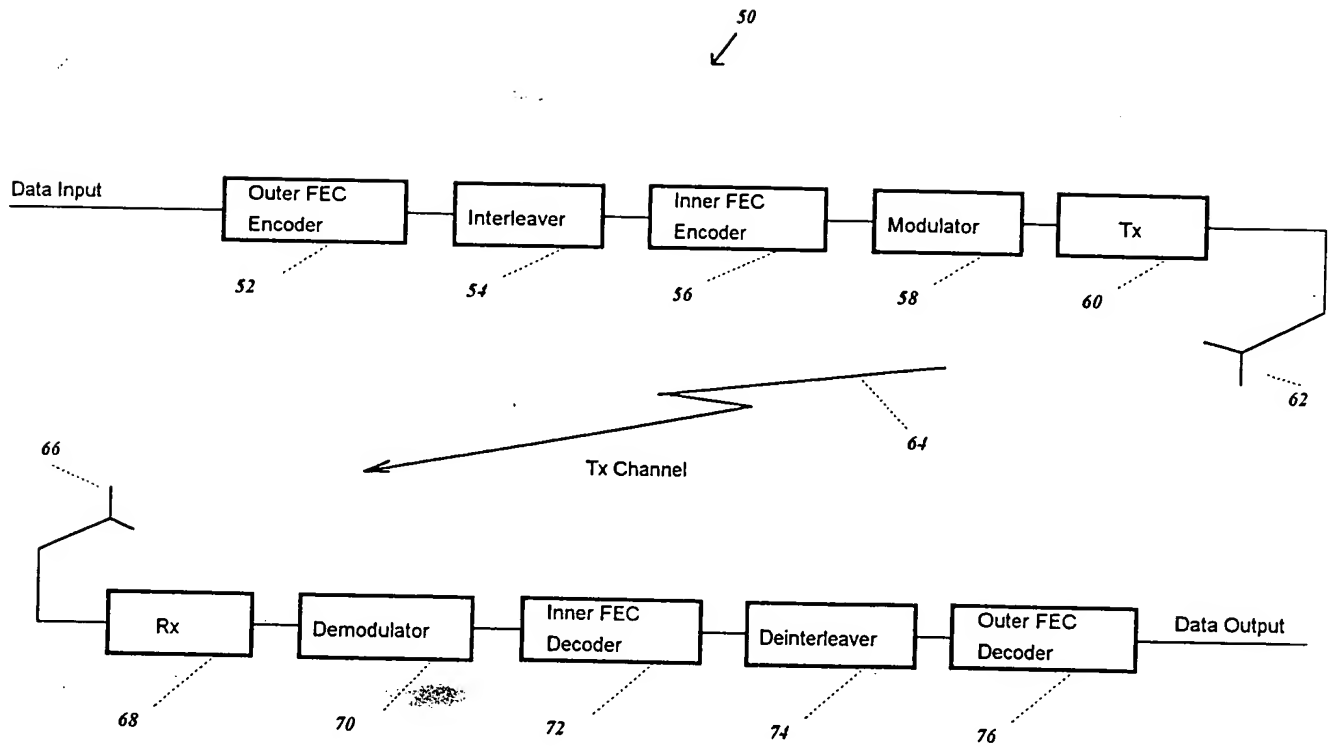


Fig. 4

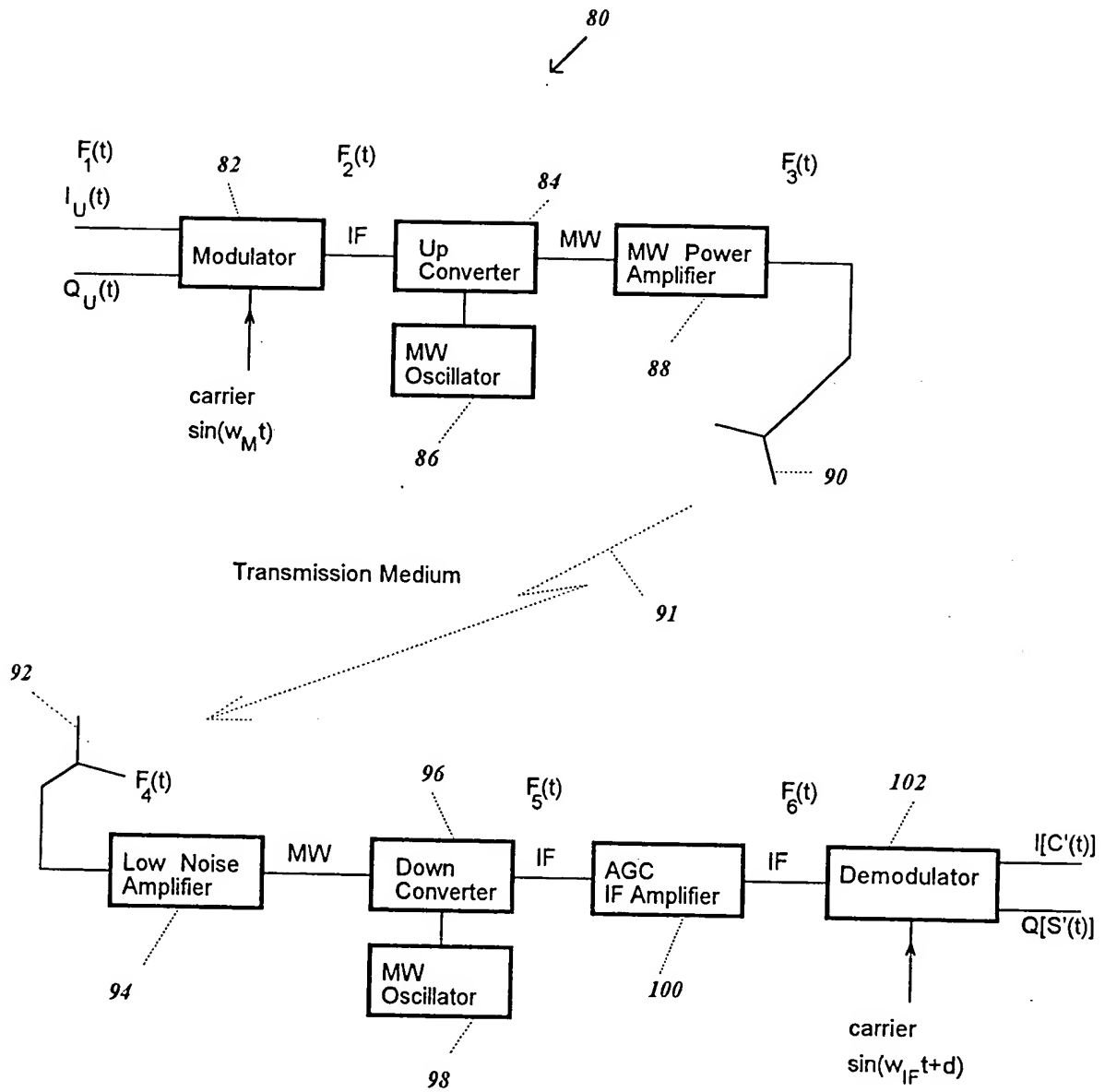


Fig. 5

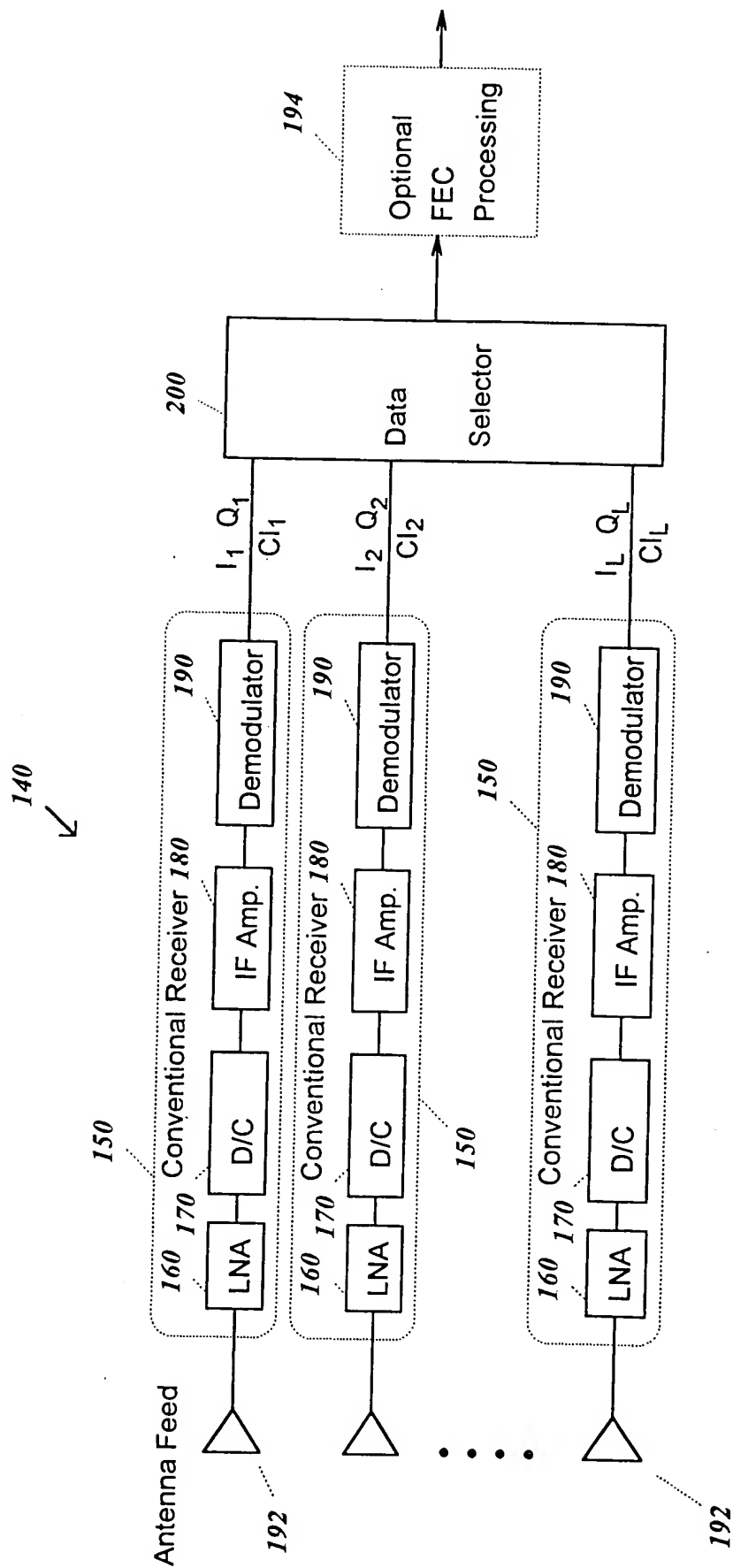


Fig. 6 Block Diagram of the Diversity Signal Data Selector System

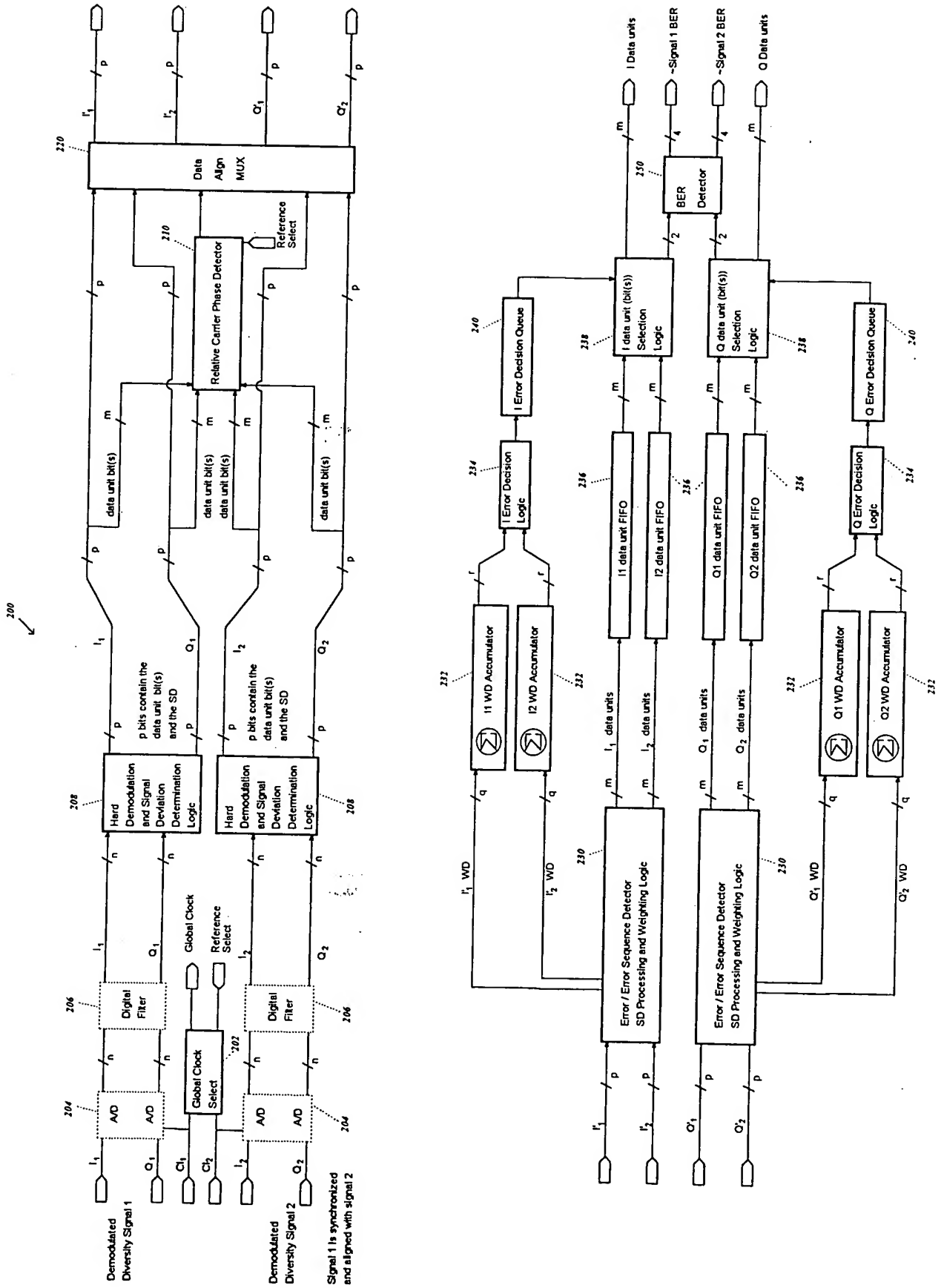


Fig. 7 Global Clock Select

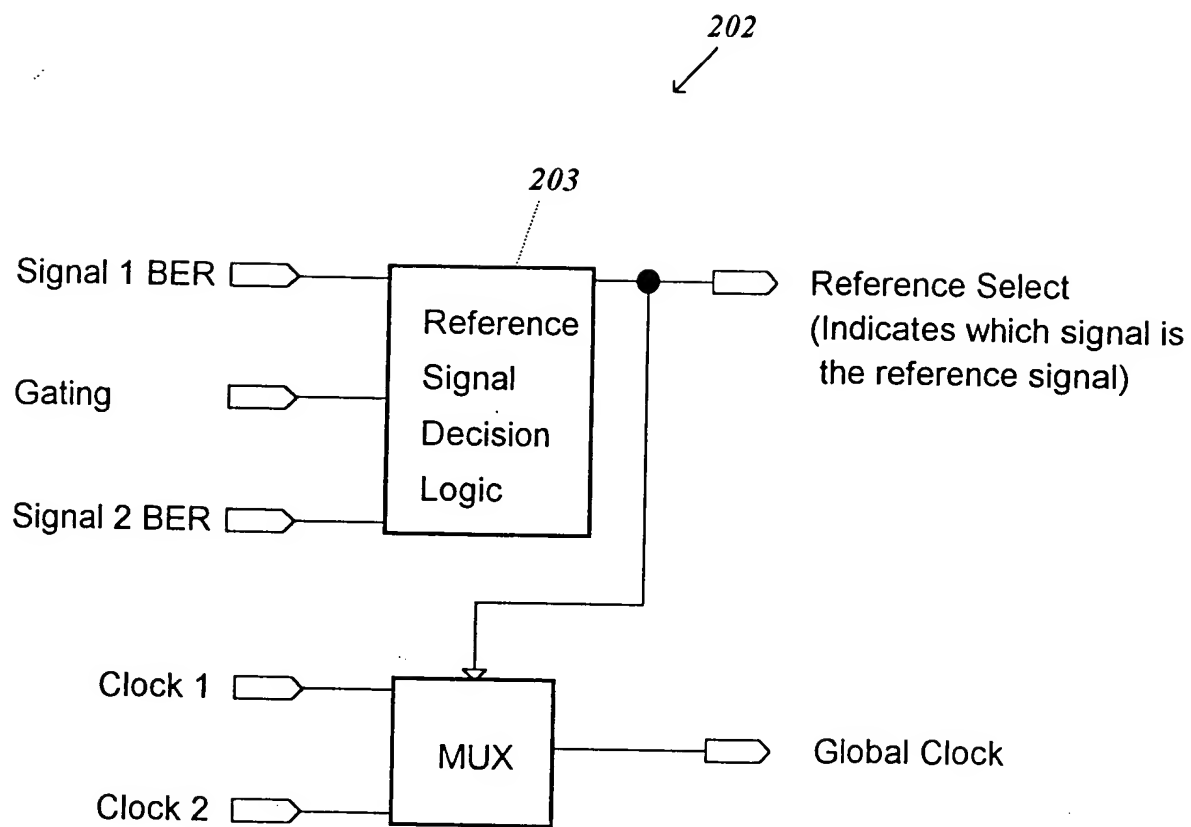
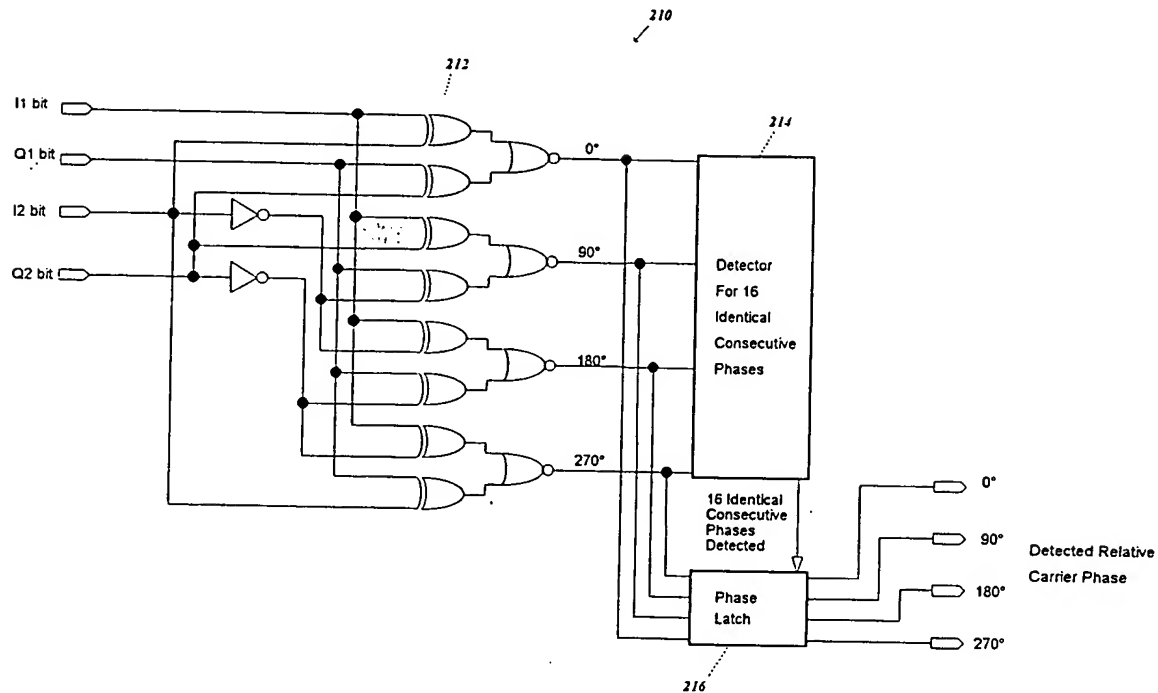


Fig. 8 Simplified Diagram of Data Alignment Circuit (QPSK or QPRS modems)

Relative Carrier Phase Detector



Data Align Multiplexer

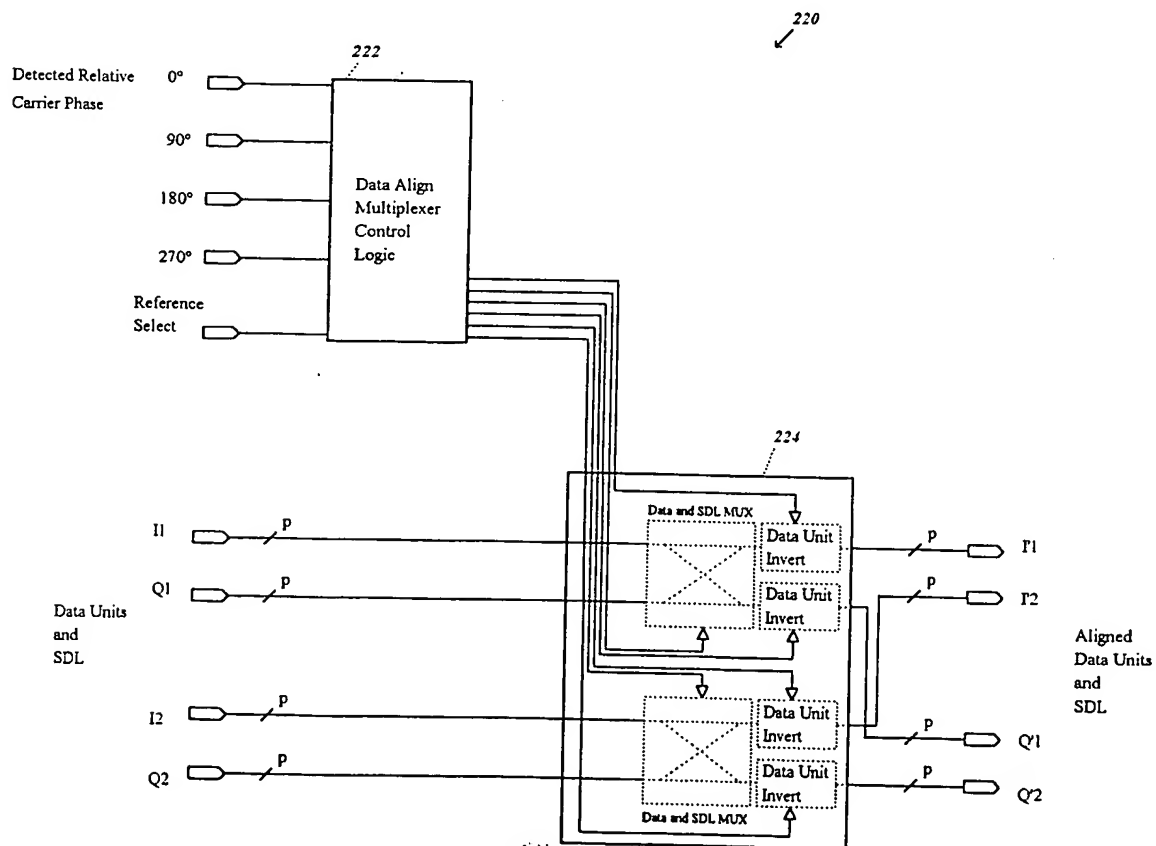


Fig. 9 BER Detector

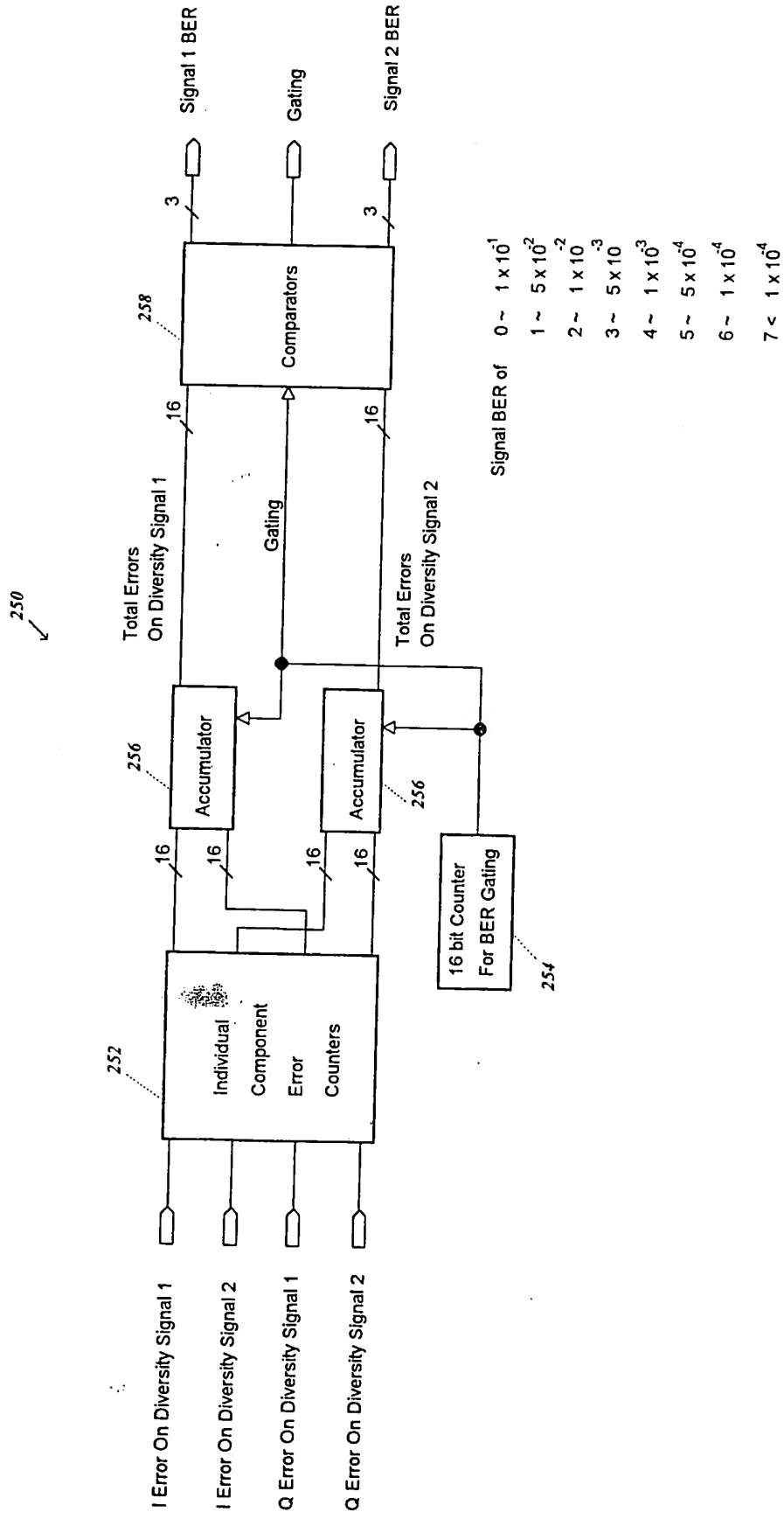


Fig. 10 Expected and Measured BER for 9QPR Radio Using Diversity Signal Selection

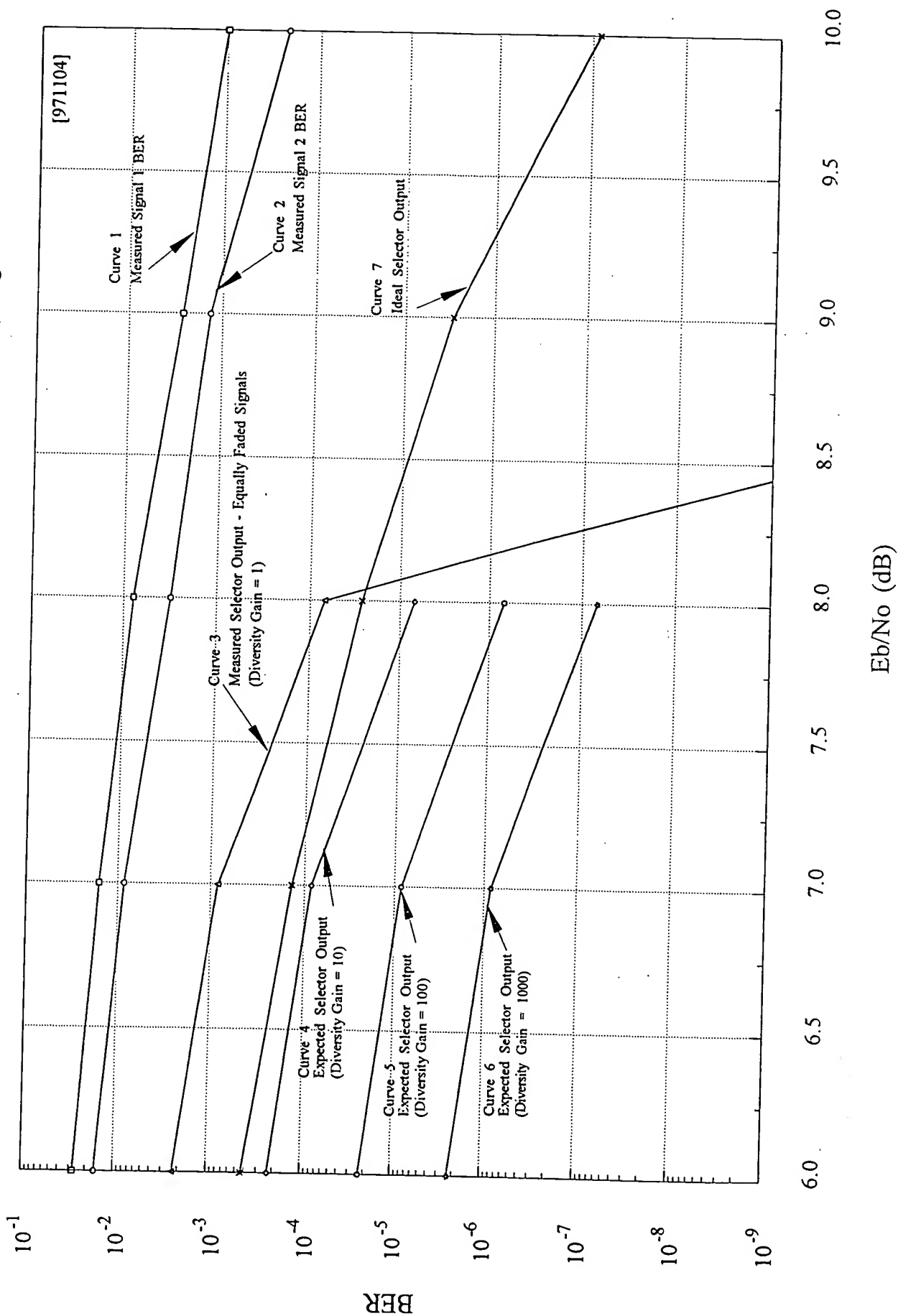


Fig. 11 Expected BER Characteristics of Unprotected and Diversity Protected QPSK Radio

